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## Chairmen's Welcome Message

We are really pleased to welcome you to the 9th International Conference on Integrated Power Electronics Systems (CIPS). This important event is held to be a technical and scientific forum for engineers and researchers engaged in all the aspects of hybrid integration and reliability of power electronics systems. We are truly delighted that you are a part of this event.

CIPS 2016 is organized by ETG, the Power Engineering Society in the VDE, and co-organized by ECPE, the European Center for Power Electronics. The program for this year's conference includes 18 sessions (17 oral and one dialog session) with the presentation of 100 technical papers: Four Keynote papers, 9 Invited papers, and 87 regular papers (33 of which are presented in the dialogue/ poster session).

The Technical Programme Committee considers the dialogue session as a very important part of the conference and a Best Poster Award will be presented in the plenary session on Thursday, March 10. A special area will disclose a couple of posters dealing with the famous „Little Box Challenge“. Google will give 1 Million US\$ to the team which has designed and built the smallest PV inverter.

- 27 of the papers come from industry,
- 32 are authored by industry and academia, and
- 41 come from research institutes.

The four keynote and 9 invited papers, authored by qualified experts coming from worldwide important industrial and academic organizations, are presenting the latest developments in various aspects of Integrated Power Electronics Systems and their reliability with particular attention to the latest results achieved in the main research centers in America, Asia, and Europe.

It is important to mention that in contrast to the other mainstream power electronics conferences,

- reliability,
  - wide band gap devices and packaging (including requirements concerning parasitics),
  - interfaces,
  - substrates and • interconnects
- are in the focus of CIPS 2016.

All papers in these proceedings have been reviewed in a two-stage review process:

1. Based on the abstract, the Technical Program Committee decided upon the acceptance of the contributions for the preliminary conference program
2. The full papers were reviewed by at least three peers who made suggestions for improving the final papers.

In addition to be printed in the CIPS proceedings by the VDE Verlag, the papers will be uploaded to the IEEE Xplore digital library and the VDE Verlag library. The papers can be applied to the IEEE Transactions on Power Electronics as well.

We hope that you are able to use this conference to advance your professional career technically, socially, and culturally. This is truly a unique opportunity and on behalf of everyone, we express our thanks to all the members of CIPS 2016 organizing committee, especially including Mr. T. Raphael, Ms. H. Altintas, all VDE, and Mr. T. Harder, and Ms. I. Bollens, from ECPE, for all the work they have done to make the CIPS 2016 to great technical Event.

L. Lorenz, T. Harder  
General Chairs

A. Lindemann, D. Silber, and E. Wolfgang  
Technical Programme Chairs