

MBMV 2019 - Programm

Montag, 8. April 2019	
12.30-13.30	Mittagssnack
13.30-13.45	Begrüßung (Carna Zivkovic und Christoph Grimm)
13.45-14.45	Keynote 1: Tobias Welp (OneSpin Solutions), <i>Certified IC Integrity: Thriving Towards Correct, Safe, Secure, and Trusted Circuits</i>
14.45-15.30	Verifikationsmethoden 1 (Moderator: Carna Zivkovic)
	P4- Tobias Seufert, Christoph Scholl: <i>fbPDR: In-depth combination of forward and backward analysis in Property Directed Reachability</i> P3- Michael Schwarz, Dominik Stoffel, Wolfgang Kunz: <i>ACCESS: HW/SW-Co-Equivalence Checking for Firmware Optimization</i>
15.30-16.00	Kaffeepause
16.00-17.30	HW/SW Systeme (Moderator: Jens Schönherr)
	P1- Görschwin Fey, Rolf Drechsler: <i>Self-Explaining Digital Systems – Some Technical Steps</i> P16- Jie Hou, Martin Radetzki: <i>Measuring NoC fault tolerance with performability</i> P10- Peer Adelt, Bastian Koppelman, Wolfgang Müller, Christoph Scheytt: <i>Analyse sicherheitskritischer Software für RISC-V Prozessoren</i> P15- Michael Mandler: <i>Logical Analysis of Distributed Systems: The Importance of Being Constructive</i>
18.00-18.30	FG -Sitzung
20.00	Abendveranstaltung
Dienstag, 09. April 2019	
09.00-10.30	Keynote 2 + Verifikationsmethoden 2 (Moderator: Christoph Scholl)
09.00-09.45	Keynote 2: Daniela Genius (Campus Pierre et Marie Curie Sorbonne Université - LIP6), <i>GdR SoC^2 und LIP6</i>
09.45-10.30	P12- Martin Köhler, Klaus Schneider: <i>Inductive Proof Rules Beyond Safety Properties</i> P18- Fin Hendrik Bahnsen, Görschwin Fey: <i>Approximation of Neural Networks for Verification</i>
10.30-11.00	Kaffeepause
11.00-12.30	Entwurfsmethodik und Optimierung (Moderator: Jürgen Ruf)
	P17- Iryna Kmitina, Nico Bannow, Christoph Grimm, Daniel Zielinski, Carna Zivkovic: <i>Optimization Framework for Hardware Design of Engine Control Units</i> P11- Heinz Riemer, Eleonora Testa, Winston Haaswijk, Alan Mishchenko, Luca Amaru, Giovanni De Micheli, Mathias Söken: <i>Logic Optimization of Majority-Inverter Graphs</i> P2- Keerthikumara Devarajegowda, Wolfgang Ecker, Wolfgang Kunz: <i>How to Keep 4-Eyes Principle in a Design and Property Generation Flow</i> P9- Jens Rudolf, Manuel Strobel, Joscha-Joel Benz, Christian Haubelt, Martin Radetzki, Oliver Bringmann: <i>Automated Sensor Firmware Development - Generation, Optimization, and Analysis</i>
12.30-13.30	Mittagssnack
13.30-15.00	System Entwicklung, Prozessoren (Moderator: Johannes Kölsch)
	P8- Markus Hedderich, Markus Heimberger, Axel Klekamp: <i>SEMAS - System Engineering Methodology for Automated Systems The world described in layers</i> P19- Christoph Grimm, Frank Wawrzik, Carna Zivkovic: <i>Ein Ansatz für die agile, verteilte Entwicklung Cyber-Physischer „Systems of Systems“</i> P5- Mohammad Rahmani Fadiheh, Dominik Stoffel, Clark Barrett, Subhasish Mitra, Wolfgang Kunz: <i>Processor Hardware Security Vulnerabilities and their Detection by Unique Program Execution Checking</i> P14- Jens Frömmer, Nico Bannow, Axel Aue, Christoph Grimm, Klaus Schneider: <i>Model-Based Configuration of a Coarse-Grained Reconfigurable Architecture</i>
15.00	Ende der Veranstaltung