

Programm des MBMV 2022

Donnerstag, 17.02.2022

09:50 Begrüßung	
10:00 Gastvortrag	<i>Session Chair: Jens Brandt</i>
Benedikt Janßen (Scheidt & Bachmann) Entwicklung von sicherheitskritischen FPGA-Designs am Beispiel von Zugsicherungssystemen	
10:45 Pause	
11:00 Session 1	<i>Session Chair: Wolfgang Müller</i>
Christian Bartsch, Stephan Wilhelm, Daniel Kästner, Dominik Stoffel and Wolfgang Kunz. Compositional Fault Propagation Analysis in Embedded Systems using Abstract Interpretation	
Lukas Mentel, Karsten Scheibler and Tino Teige. Detection and Elimination of Constants to Strengthen k-Induction	
Tobias Seufert, Christoph Scholl, Arun Chandrasekharan, Sven Reimer and Tobias Welp. Making PROGRESS in Property Directed Reachability	
12:00 Pause	
12:15 Session 2	<i>Session Chair: Jens Schönherr</i>
Mehran Goli and Rolf Drechsler. Simulation-based Verification of SystemC-based VPs at the ESL	
Sebastian Prebeck, Sathya Ashok, Mounika Vaddeboina, Keerthikumara Devarajegowda and Wolfgang Ecker. A Scalable, Configurable and Programmable Vector Dot-Product Unit for Edge AI	
Omair Rafique and Klaus Schneider. Data-aware Global Scheduling for Dataflow Process Networks	
13:15 Mittagspause	
14:15 Session 3	<i>Session Chair: tbd</i>
Klaus Schneider, Anoop Bhagyanath and Julius Roob. Virtual Buffers for Exposed Datapath Architectures	
Lucas Klemmer and Daniel Große. Programmable Waveform Analysis using the Domain Specific Language WAL	
15:15 Pause	
15:30 Session 4	<i>Session Chair: tbd</i>
Niklas Bruns, Vladimir Herdt, Eyck Jentzsch and Rolf Drechsler. RISC-V Processor Verification with Coverage-guided Aging	
Ming Hu, Leonore Winterer and Ralf Wimmer. Diagnosing Partially Observable Markov Decision Processes	
Alireza Mahzoon and Rolf Drechsler. Polynomial Formal Verification of Complex Multipliers	
16:30 Abschluss und Ausblick	

im Anschluss: Sitzung der GMM/ITG/GI-Fachgruppen 3 und 4