



**European Future Technology Summit  
(EFTS)**

**Brussels 2025**

**01.-03.09.2025**

# European Future Technology Summit Brussels 2025

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## Europe's Comeback: Microelectronics, Resilience and Future Technology under the EU Chips Act

### European Future Technology Summit (EFTS)

- Addressed to all Young Engineers (Students and Young Professionals) of the EUREL Member Association
- Focus on future technologies & interactive workshops
- Gives an impression of the work of the European Institutions such as the European Parliament, the European Commission or the Council
- Serves the opportunity to meet and network with people from other European countries
- Including: Winners of the PhD Best Paper Award and the International Management Cup

### VDE Summer Reception

- Panel Discussion with Electrical, Electronic and Information Technology Engineers and Politicians and Stakeholder followed by a Social Network Event
- International Networking and Exchange

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## Europe's Comeback: Microelectronics, Resilience and Future Technology under the EU Chips Act

### Monday, 01.09.2025

- Midday: individual arrival  
B&B Brussels Centre Louise, Rue Paul Spaak 15 -1000 Brussels
- From 3:00 pm: 3 workshops at Headquarters of CEN CENELEC
- From 7:00 pm: Dinner & Networking

### Tuesday, 02.09.2025

- 4 Workshops
- Official evening event „VDE Summer Reception“ at the Bavarian Representation with a panel discussion, award ceremony EUREL IMC & PhD Best Paper Award, followed by an invited reception

### Wednesday, 03.09.2025

- European Parliament Tour
- Guided Sightseeing-Tour through Brussels
- Individual departure

# European Future Technology Summit Brussels 2025

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## Europe’s Comeback: Microelectronics, Resilience and Future Technology under the EU Chips Act

### Monday, 01.09.2025 – Workshops

CEN and CENELEC - Rue de la Science 23, 1040 Brussels

Time	Programme
3:45 pm	<b>Welcome</b> <i>Fabienne Dezutter, Secretary General EUREL, Political Affairs Officer VDE</i>
4:00 pm – 4:45 pm	<b>High-performance semiconductors „Made in Saxony“ - how a German federal state contributes to strengthening European competitiveness in the future.</b> <i>Christopher Lang, Deputy Director, Representation of the Free State of Saxony to the European Union</i>
5:00 – 5:45 pm	<b>Chips Joint Undertaking - Its role in the European semiconductor ecosystem</b> <i>Eric Fribourg-Blanc, Senior Programme Officer, Chips Joint Undertaking</i>
7:00 pm	<b>Dinner at L’Utime Atome, Rue Saint-Boniface 14</b> <i>Get-Together &amp; Networking</i>

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## Europe's Comeback: Microelectronics, Resilience and Future Technology under the EU Chips Act

**Tuesday, 02.09.2025 – Workshops**

Silversquare – Avenue Louise 231, 1050 Brussels

Time	Programme
10:00 – 10:45 am	<b>Resilience of Communication Networks</b> <i>Sigurd Schuster, Project Officer, Board of VDE ITG</i>
11:00 – 11:45 am	<b>How Can Standards Support Europe's Comeback in Power Grid Resilience and Microelectronics?</b> <i>Cinzia Missiroli, Director Standardization, Deputy Director General, CEN CENELEC</i>
12:00 – 12:45 pm	<b>Lunchbreak – Catering</b> <i>Get-Together &amp; Networking</i>
12:45 – 1:30 pm	<b>Chips Made in Europe: Policy Frameworks and the Role of European Players</b> <i>René Schröder, Director General, ESIA</i>
1:30 – 2:15 pm	<b>Hidden Electronics: Foundations of Smart Systems</b> <i>Prof. Dr. Christoph Kutter, Director, Fraunhofer Institut EMFT</i>

# European Future Technology Summit Brussels 2025

**Tuesday, 02.09.2025 – Evening Event „VDE Summer Reception“**

Bavarian Representation – Rue Wirtz 77, 1040 Brussels

## **Semiconductors Made in Europe: Securing competitiveness and technological sovereignty in a global context**

Time	Programme
6:00 – 6:30 pm	<b>Admission and Registration</b>
6:30 – 6:50 pm	<p><b>Opening Speech: Tobias Gotthardt</b>, MdL, State Secretary for Economic Affairs, Regional Development and Energy</p> <p><b>Welcome: Dr. Armin Hartmuth</b>, Head of Bavarian State Representation in Brussels</p> <p><b>Keynote 1: Ansgar Hinz</b>, CEO Technology Organisation VDE</p> <p><b>Keynote 2: Pierre Chastanet</b>, EU-COM/ Head of Unit Microelectronics and Photonics Industry</p>
6:50 – 7:30 pm	<p><b>Panel Discussion</b></p> <p>Prof. Dr. Christoph Kutter (Director Fraunhofer Institut EMFT), Ansgar Hinz (CEO VDE), Pierre Chastanet (EU-COM/EU COM/ Head of Unit Microelectronics and Photonics Industry)</p> <p>Moderated by Kristine August</p>
07:30 – 10:00 pm	<p><b>Social Event VDE Summer Reception &amp; Award ceremony followed by dinner</b></p> <p>EUREL TOPSIM International Management Cup</p> <p>EUREL PhD Best Paper Award</p>

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**Wednesday, 03.09.2025**

Time	Programme
10:30 am	<b>Visit of the European Parliament with Moritz Körner MEP</b> <i>Member of the European Parliament, Renew Europe</i>
12:15 – 2:30 pm	<b>Guided Sightseeing-Tour Brussels</b>
From 3:00 pm	<b>Individual Departure</b>

# EUREL VDE

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